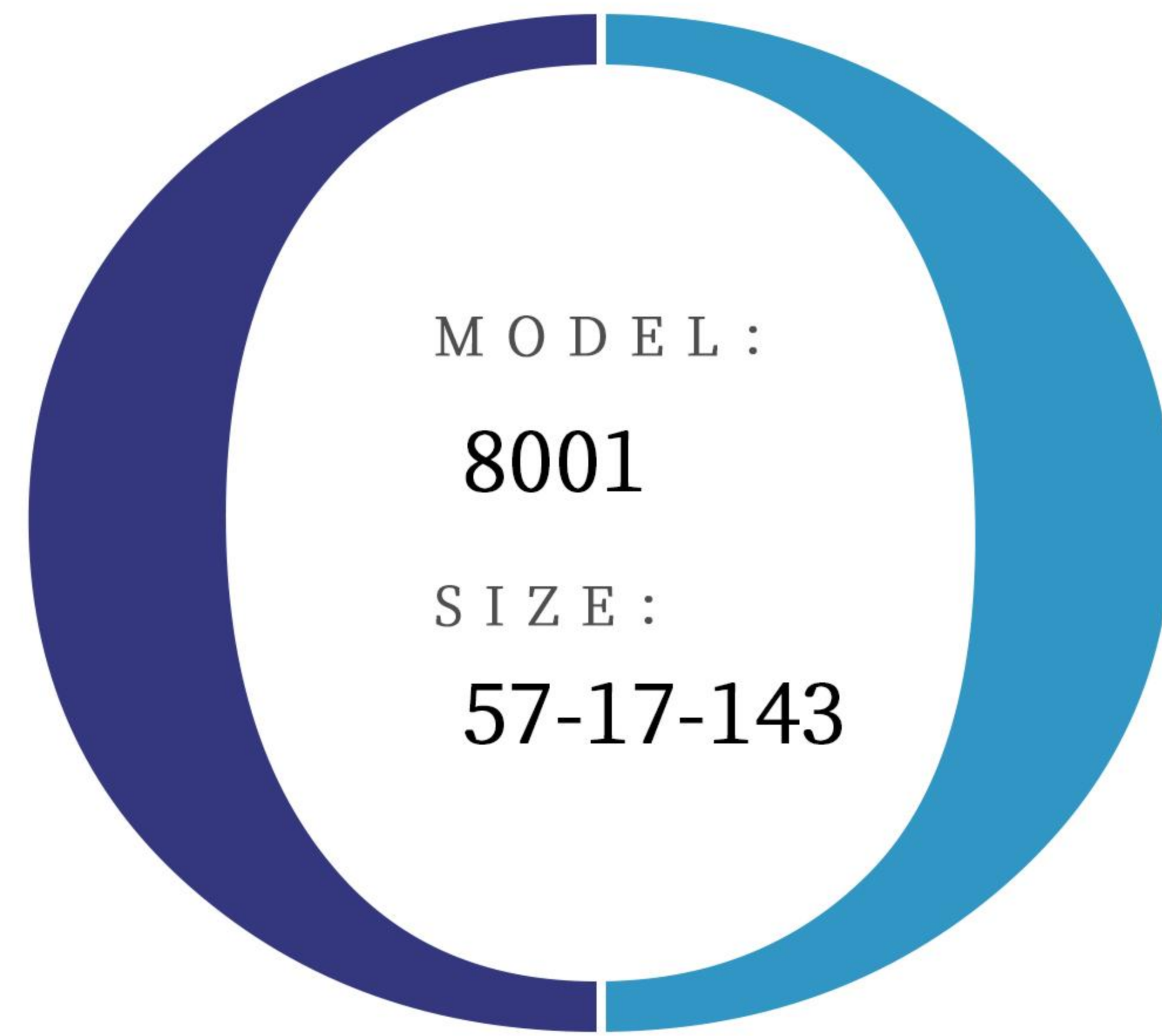




COLOR DISPLAY



C1



C2



C3

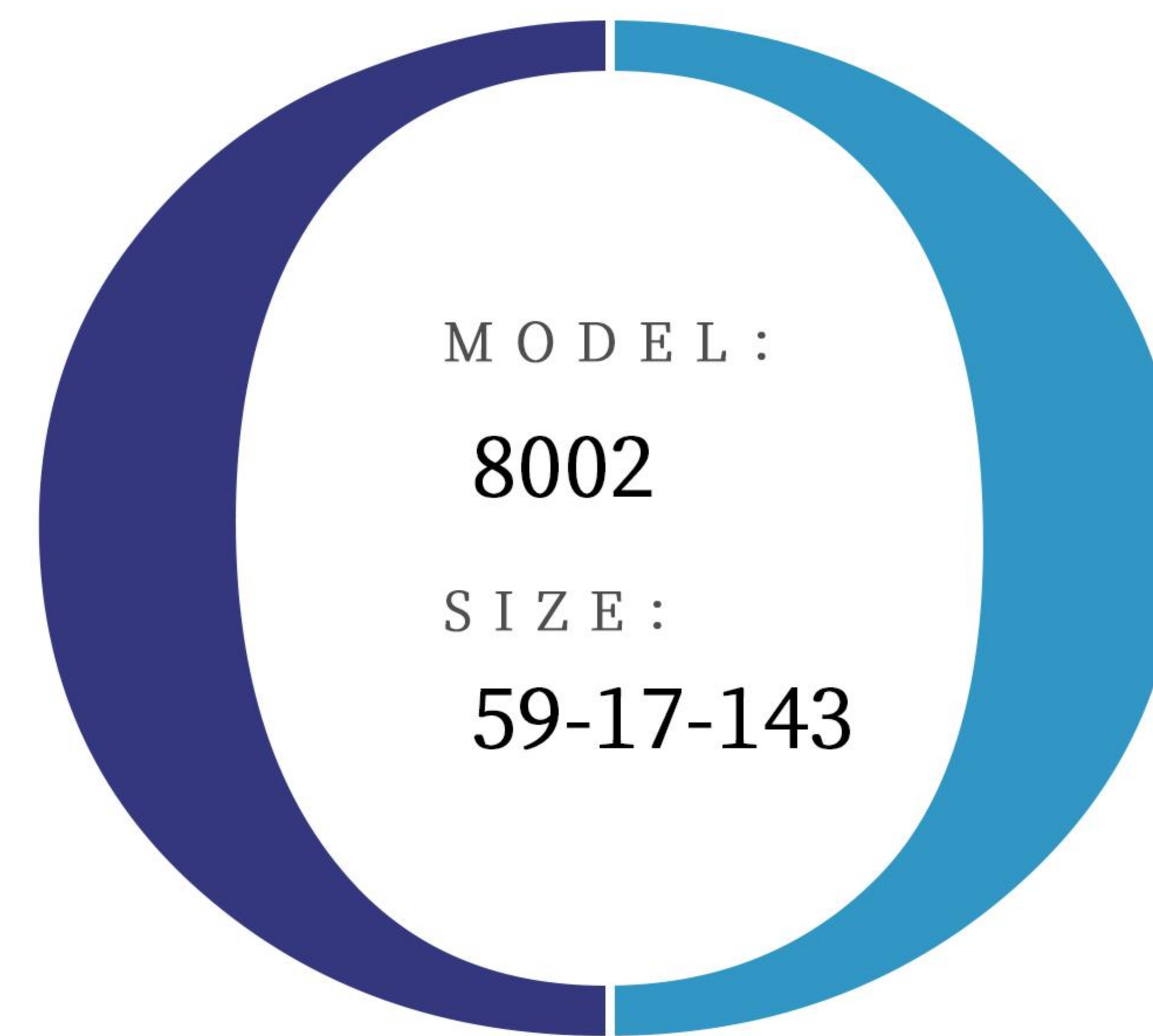


C4



C5





COLOR DISPLAY



C1



C2



C3

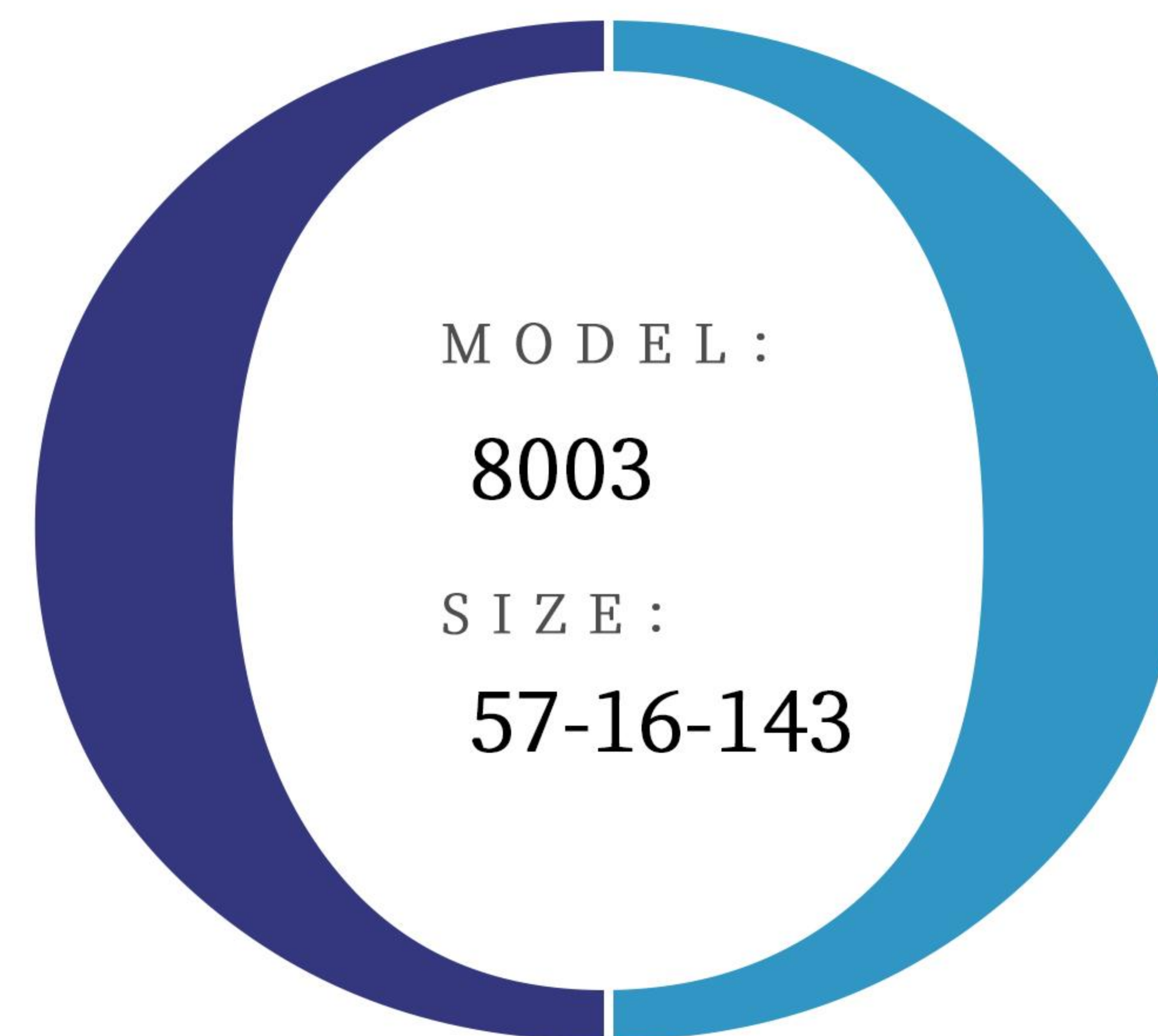


C4



C5





COLOR DISPLAY



C1



C2



C3

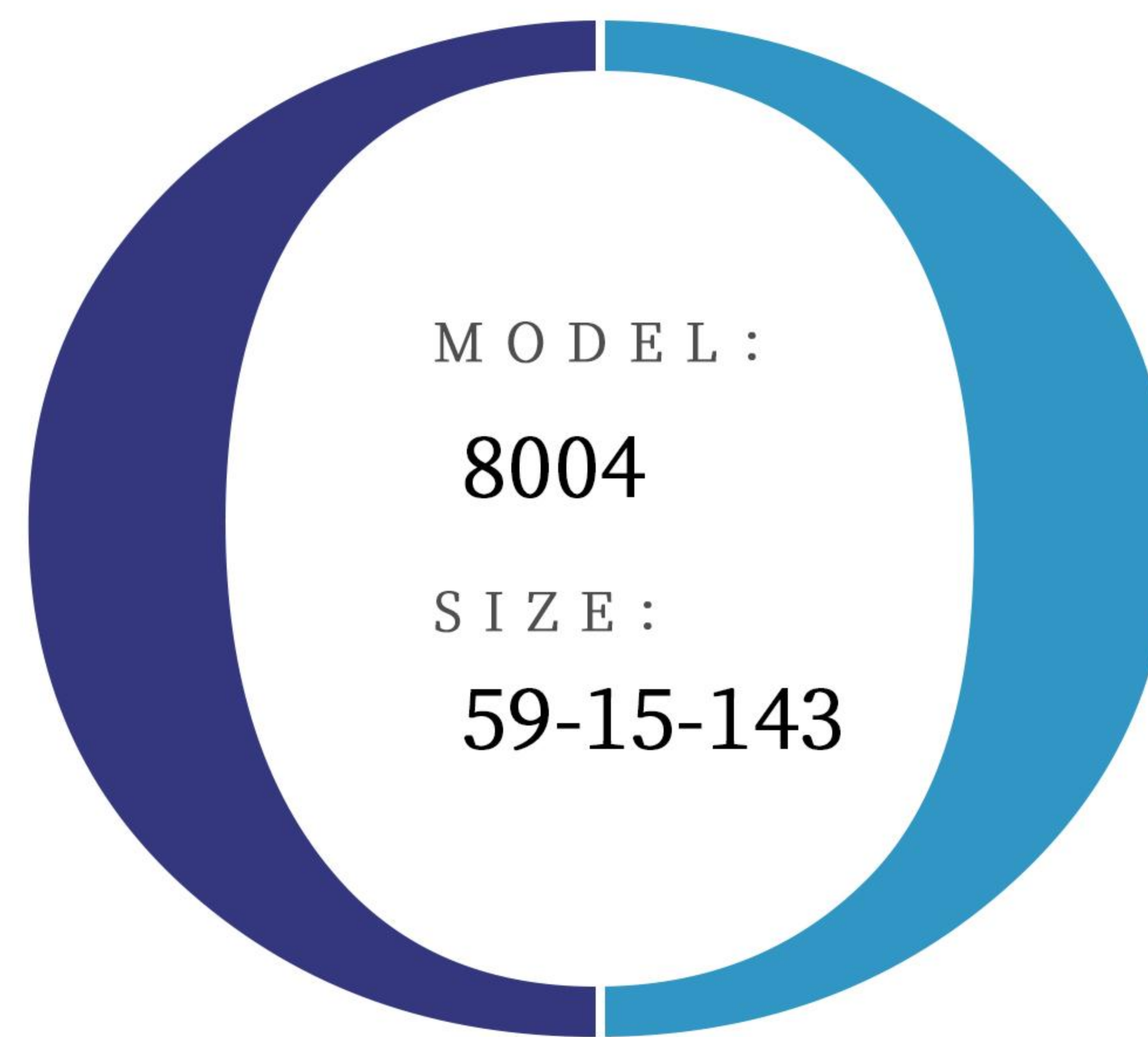


C4



C5





COLOR DISPLAY



C1



C2



C3

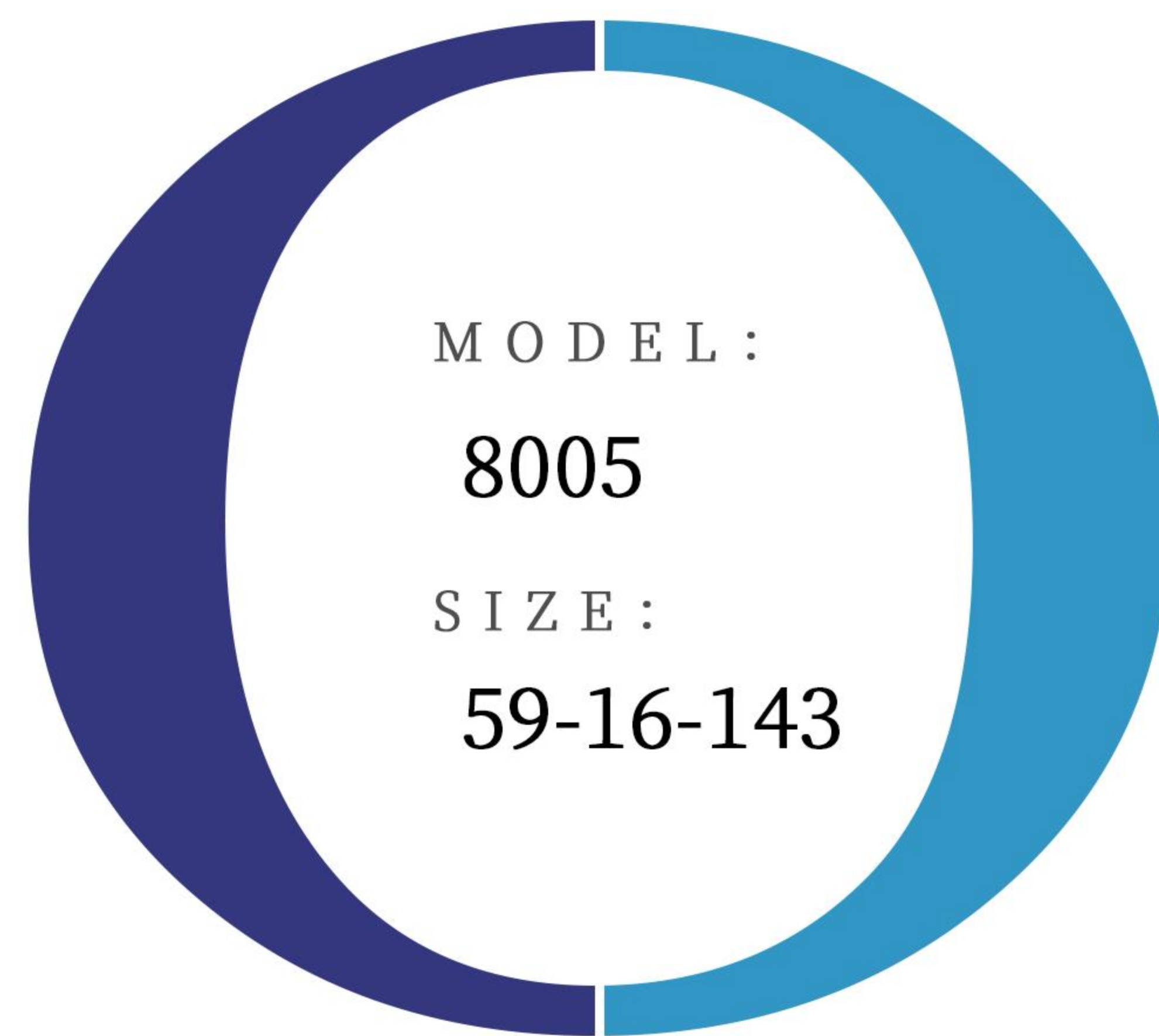


C4



C5





COLOR DISPLAY



C1



C2



C3



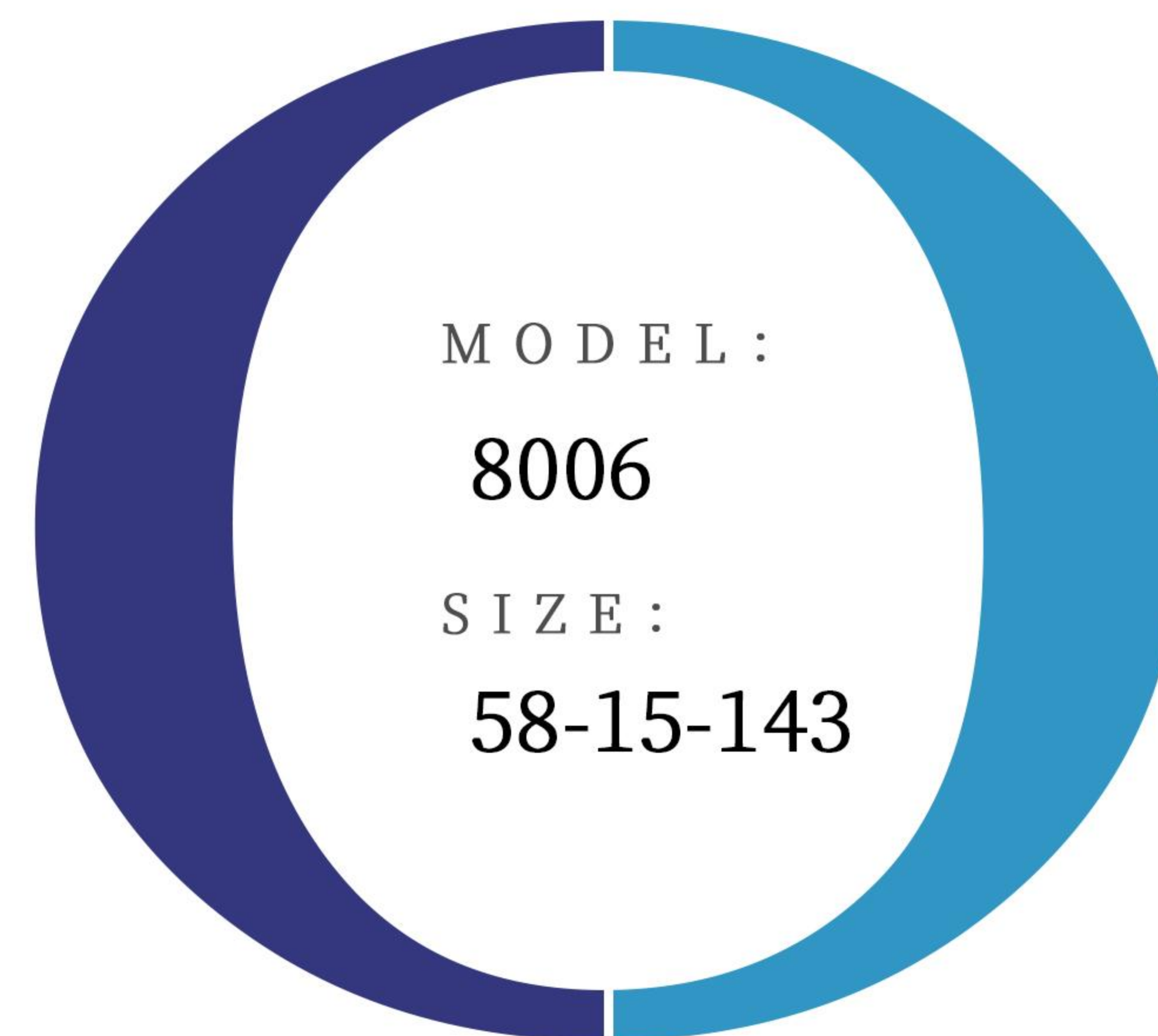
C4



C5







COLOR DISPLAY



C1



C2



C3

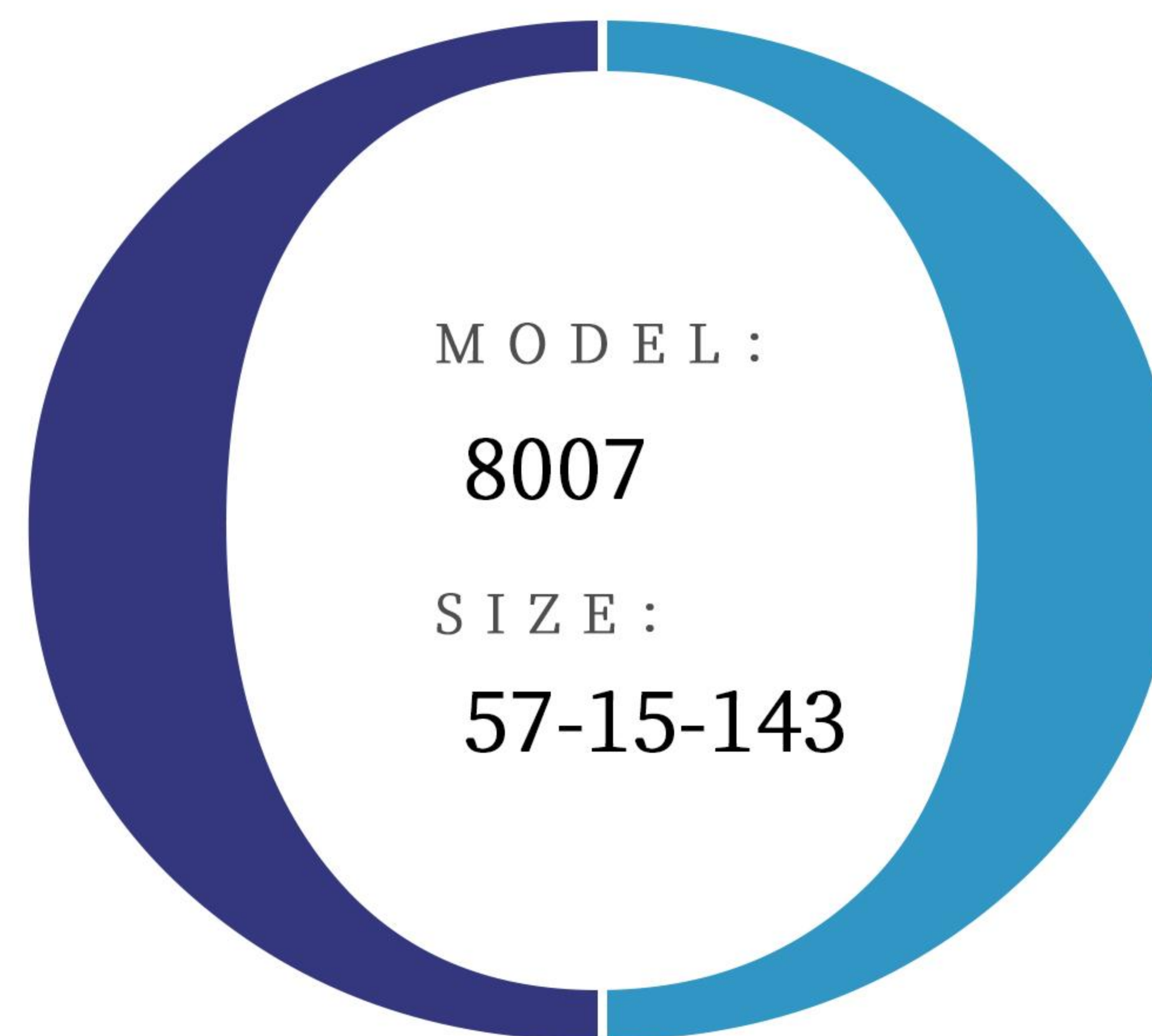


C4



C5





COLOR DISPLAY



C1



C2



C3



C4



C5







COLOR DISPLAY



C1



C2



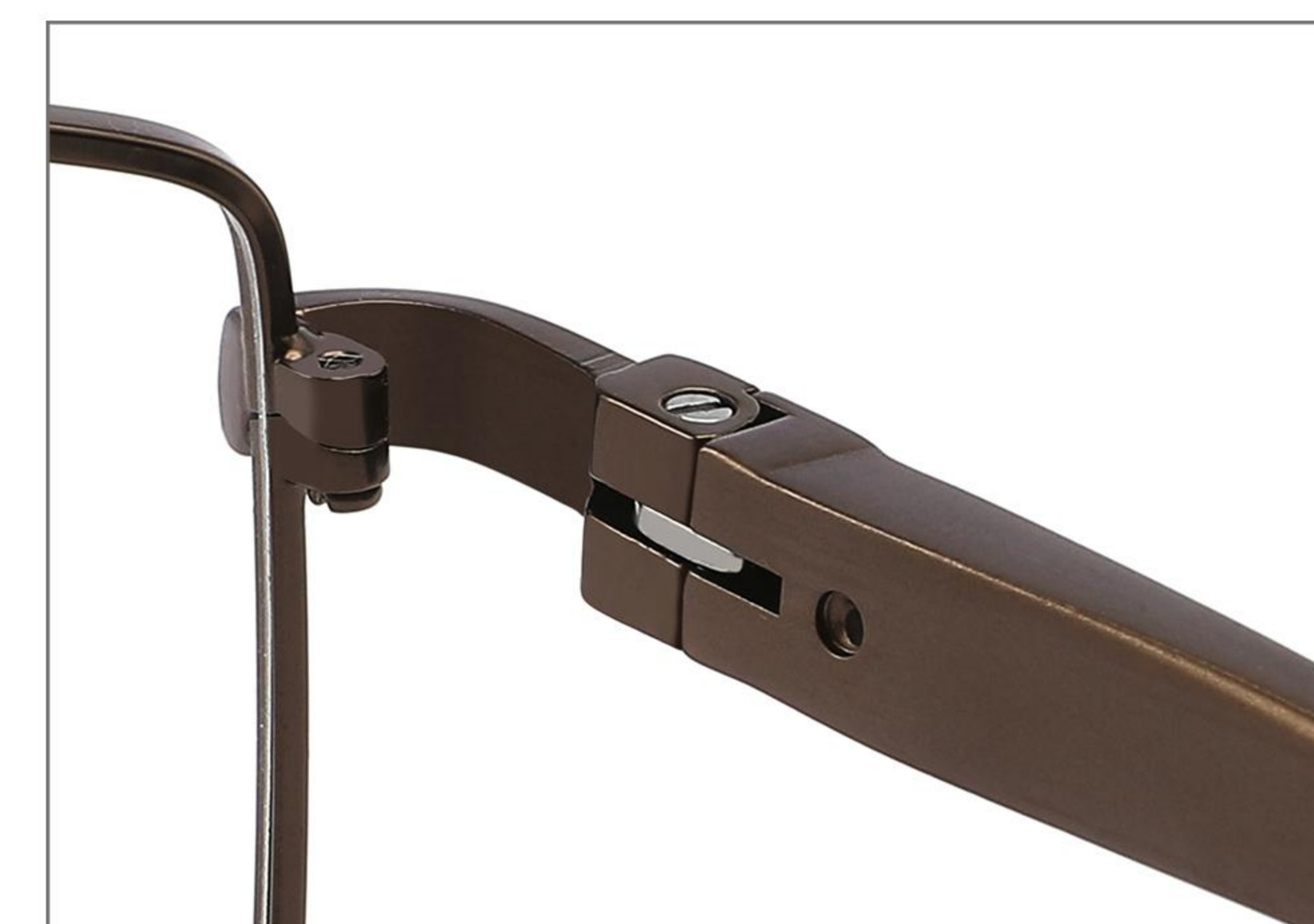
C3



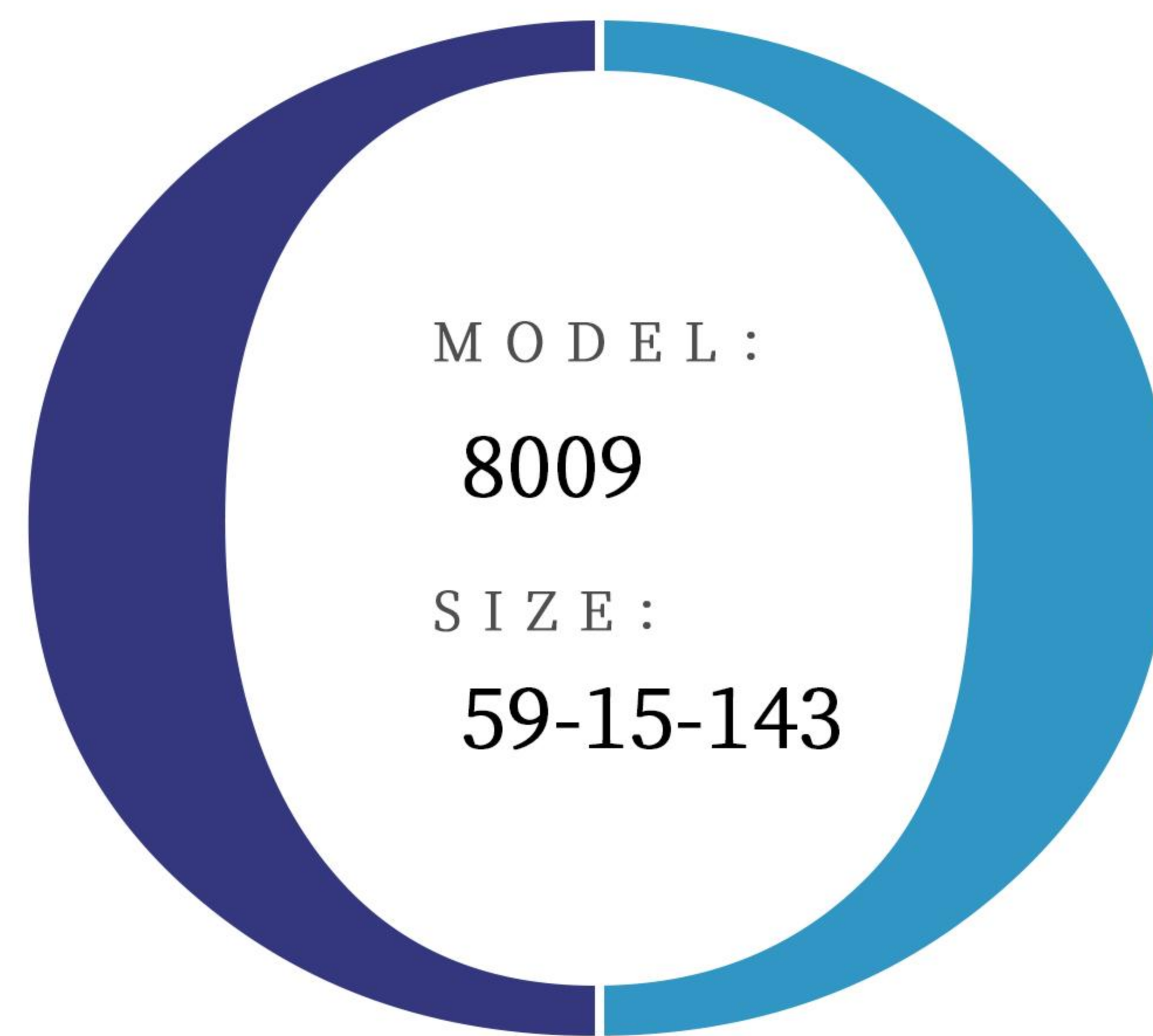
C4



C5







COLOR DISPLAY



C1



C2



C3

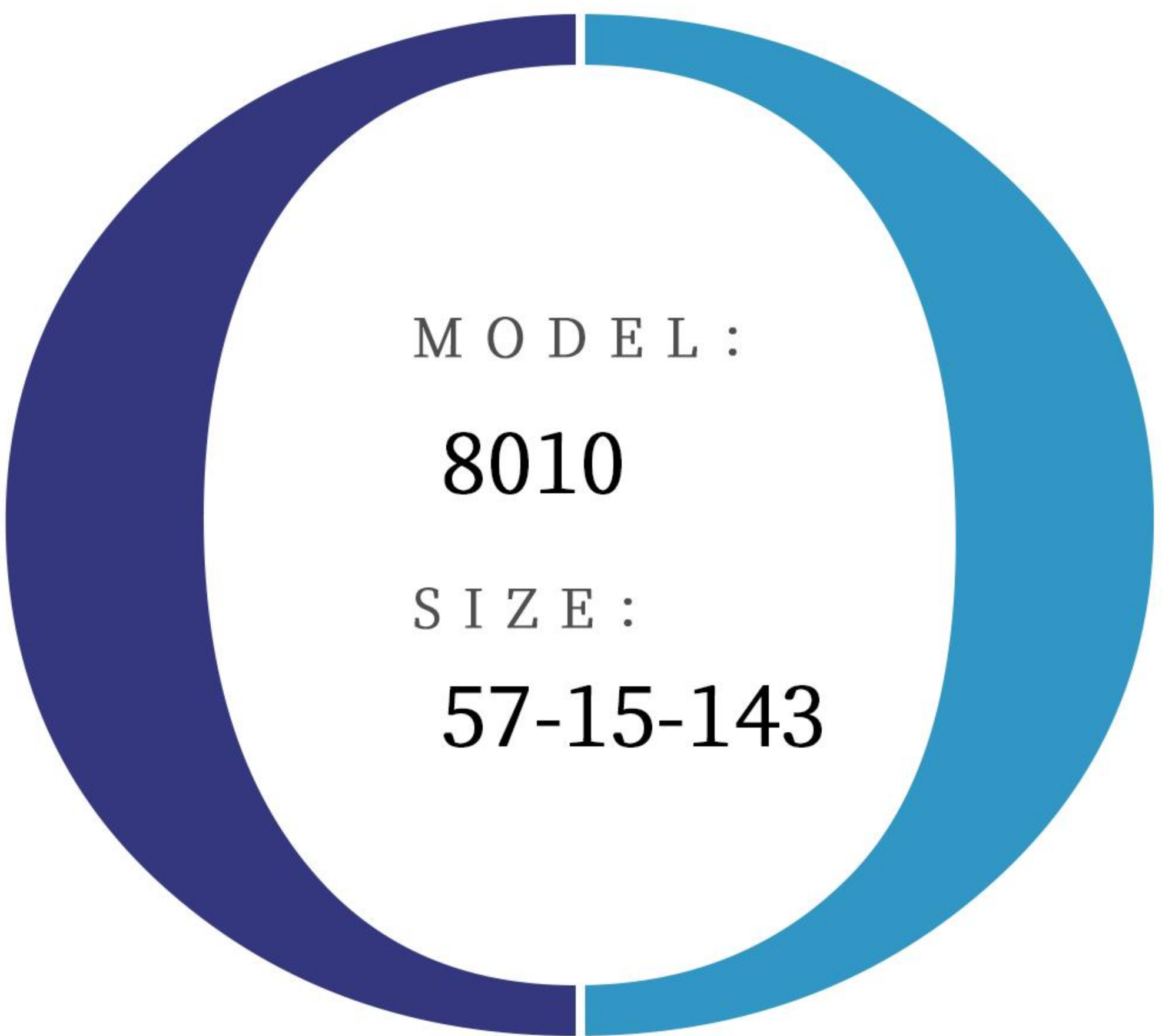


C4



C5





COLOR DISPLAY



C1



C2



C3



C4



C5

